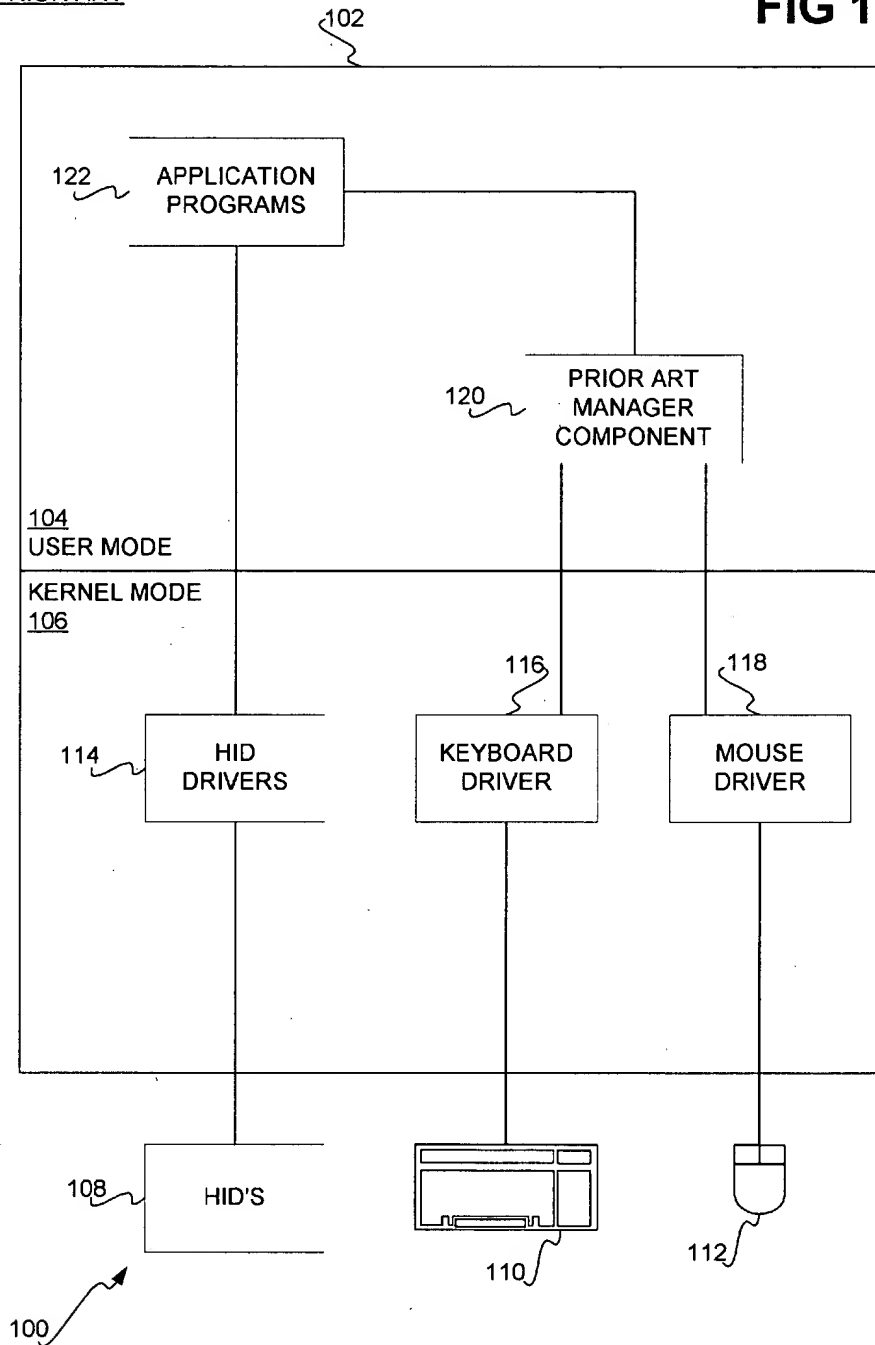


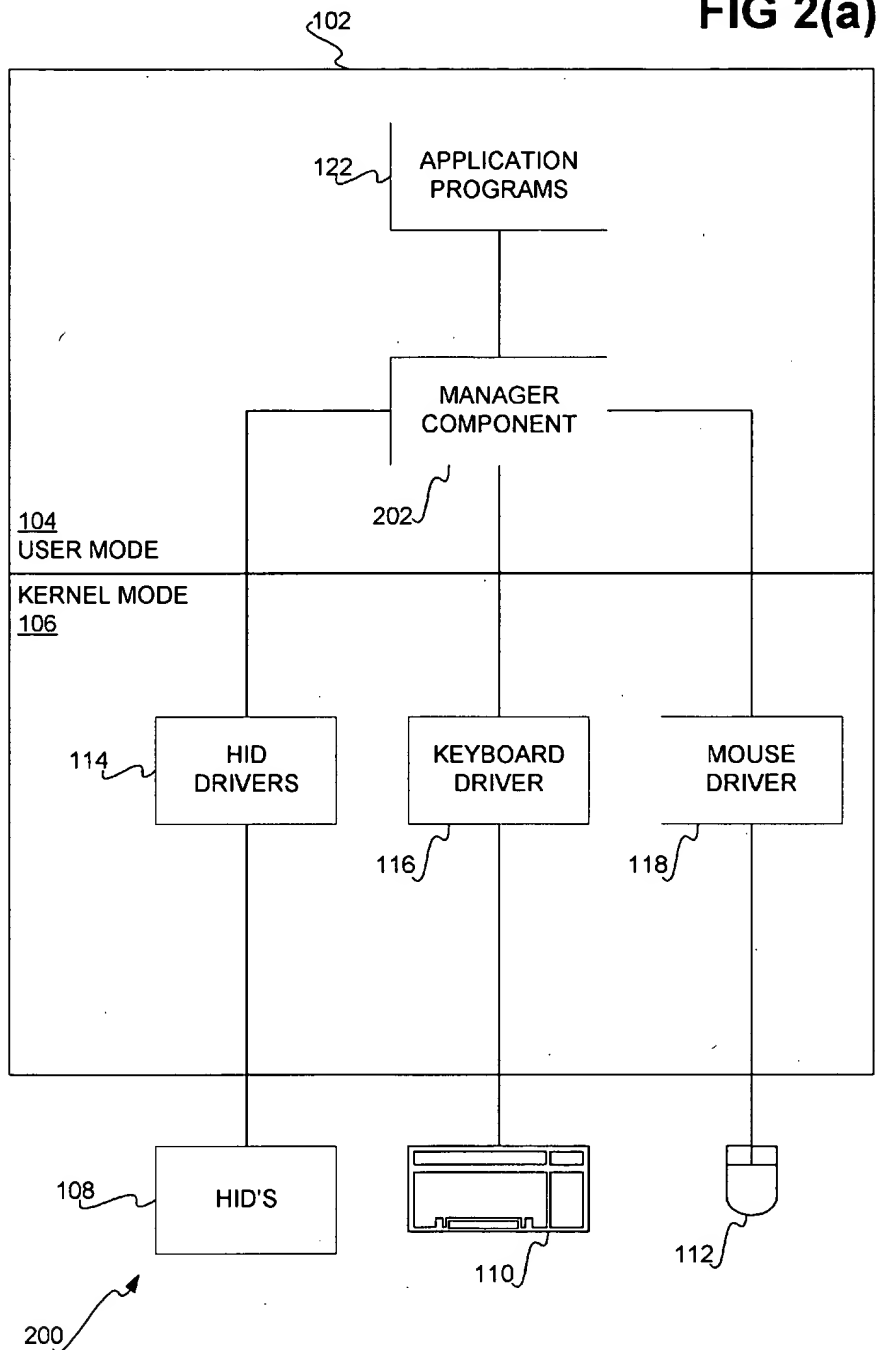
PRIOR ART

FIG 1



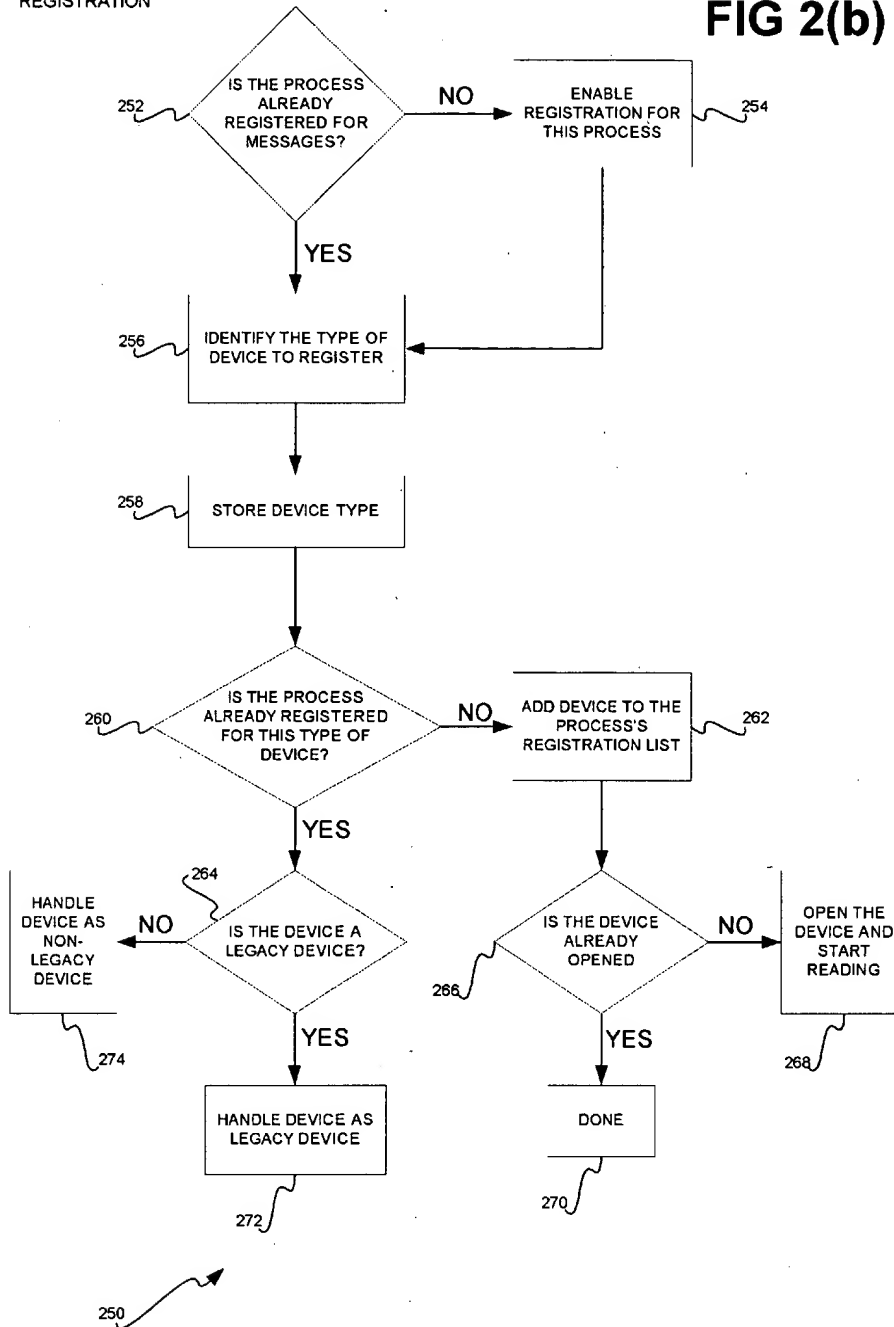
096132-060401

FIG 2(a)



09681782.060401

FIG 2(b)



09681782-060401

FIG 2(c)

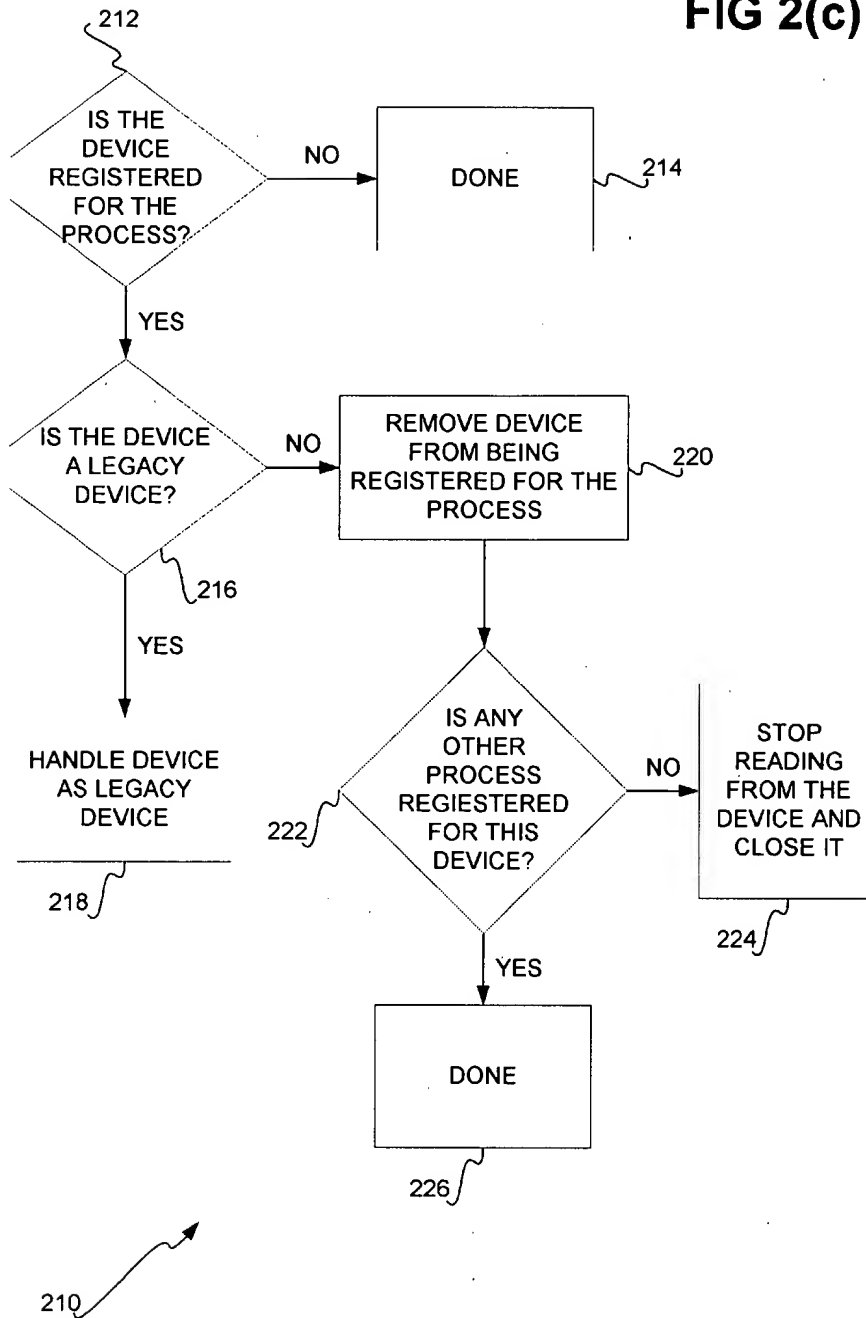
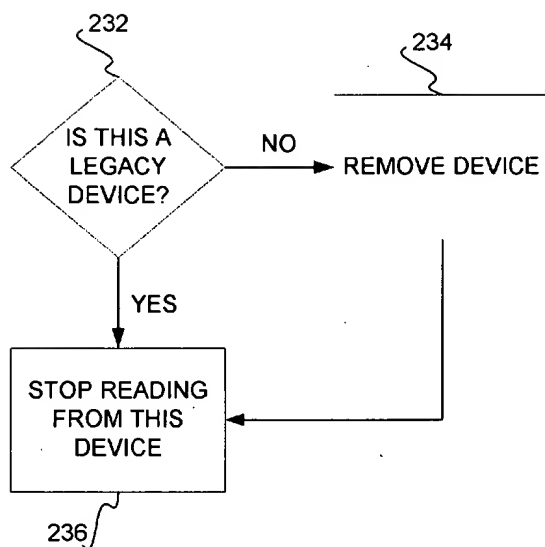


FIG 2(d)

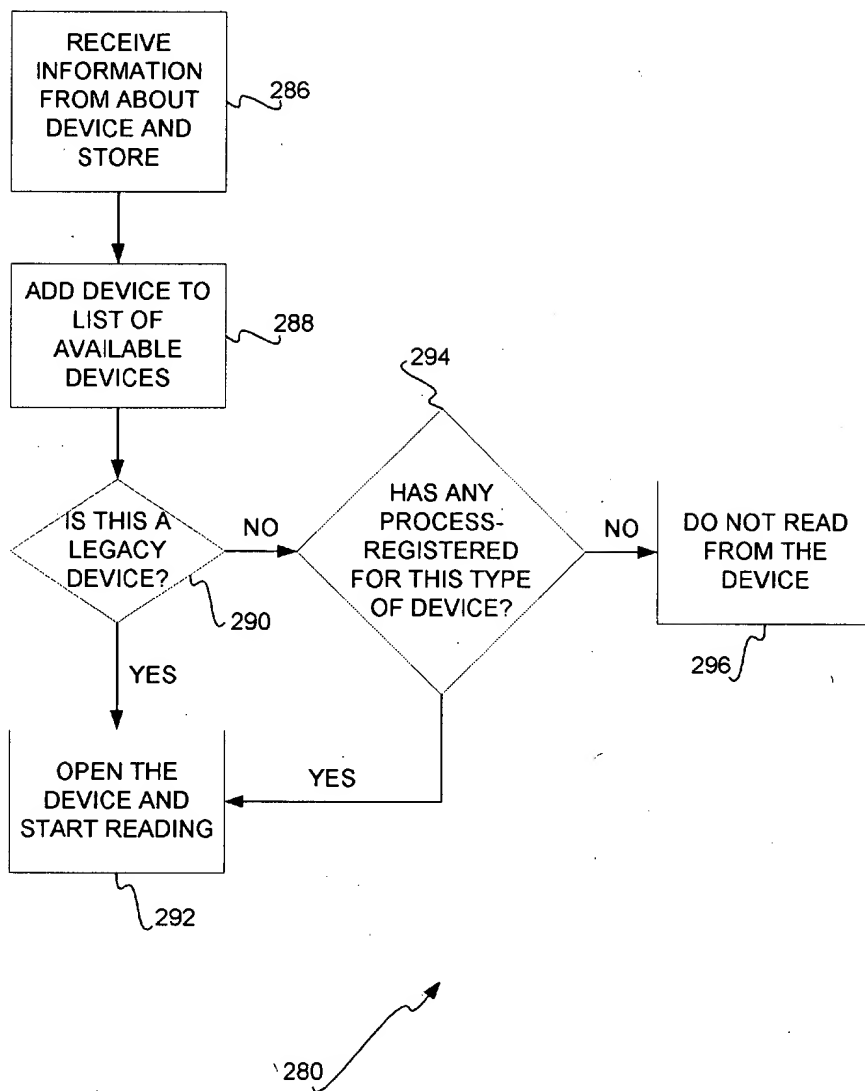


230

09681732-060401

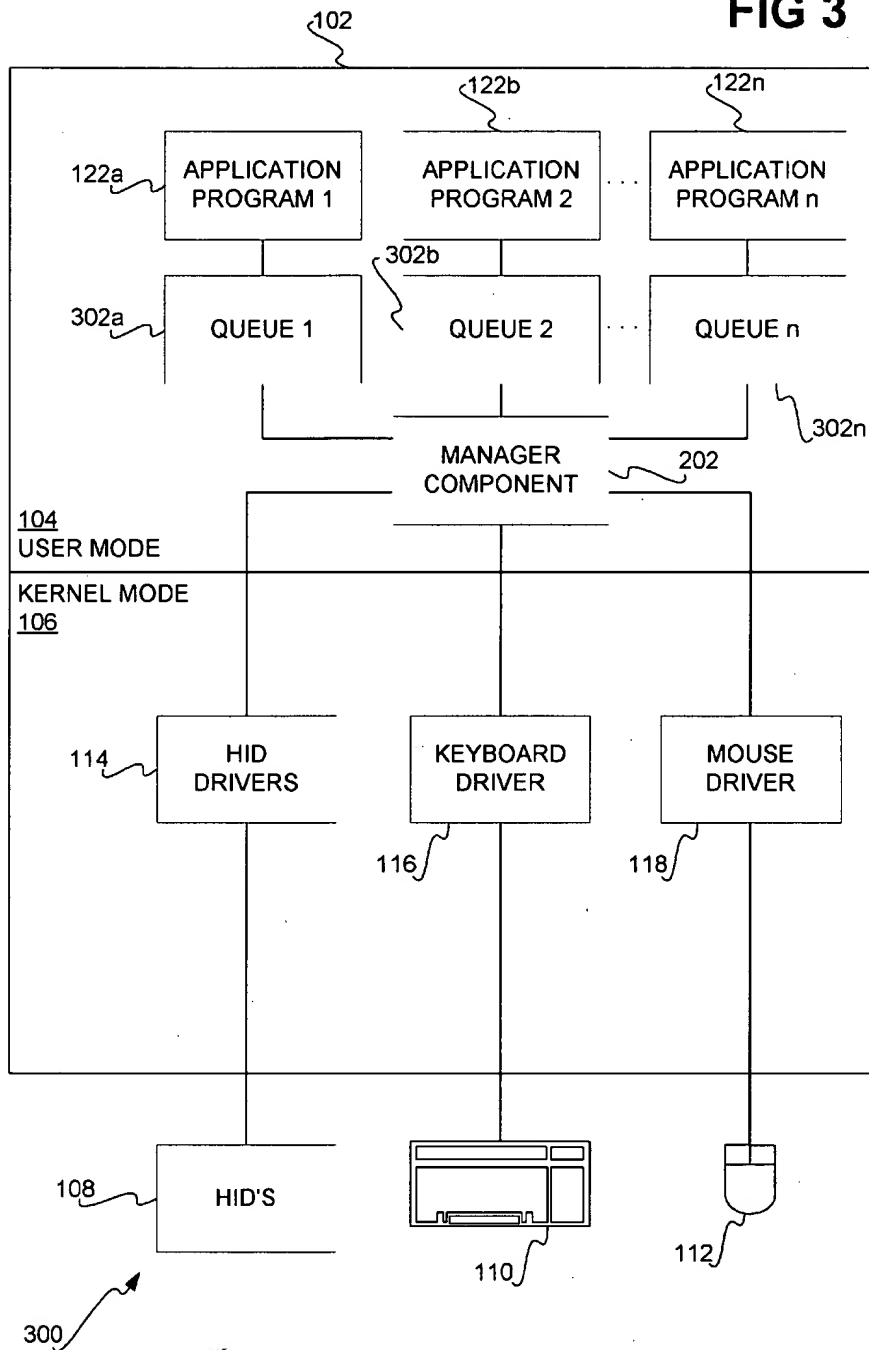
DEVICE ADDITION

FIG 2(e)



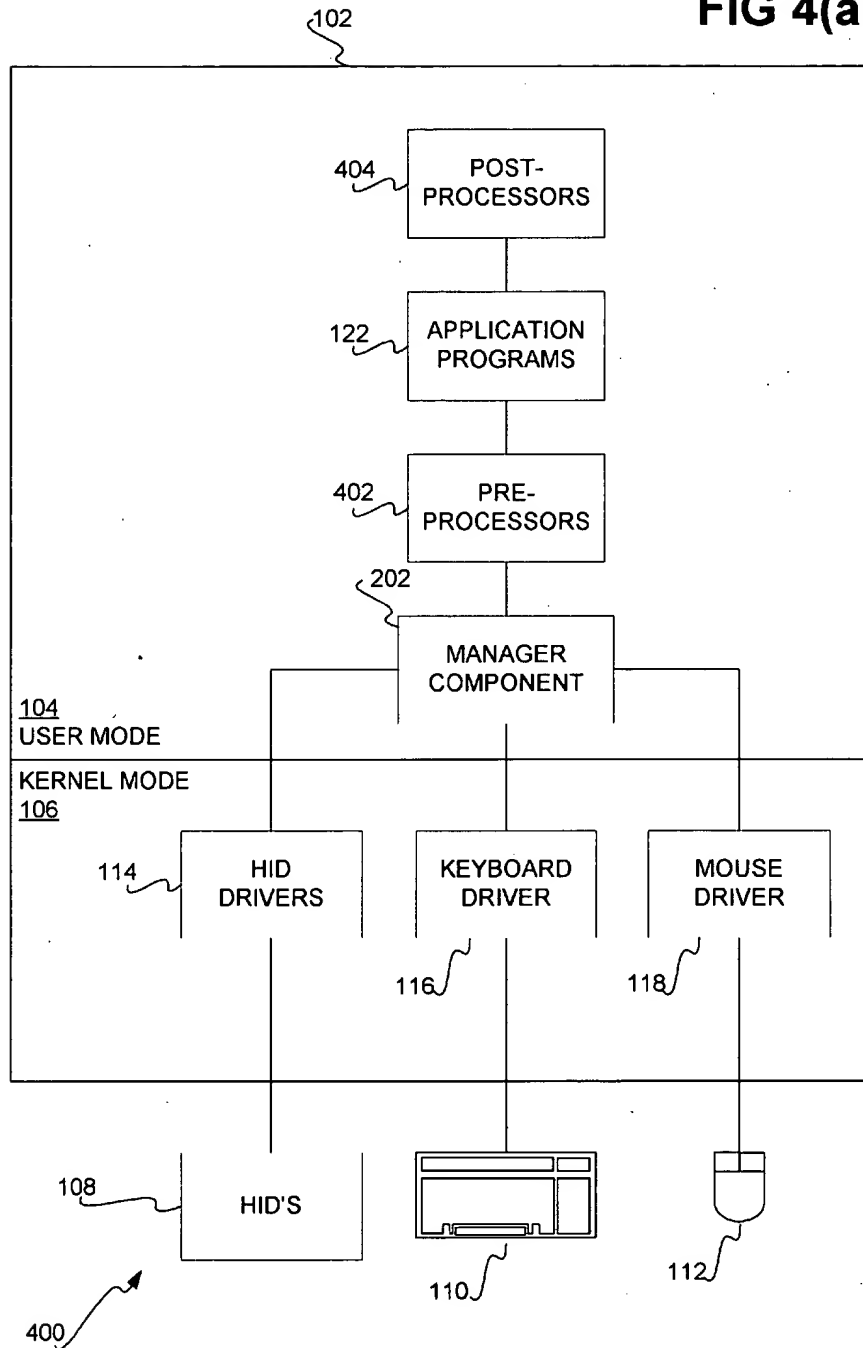
09631782-060401

FIG 3



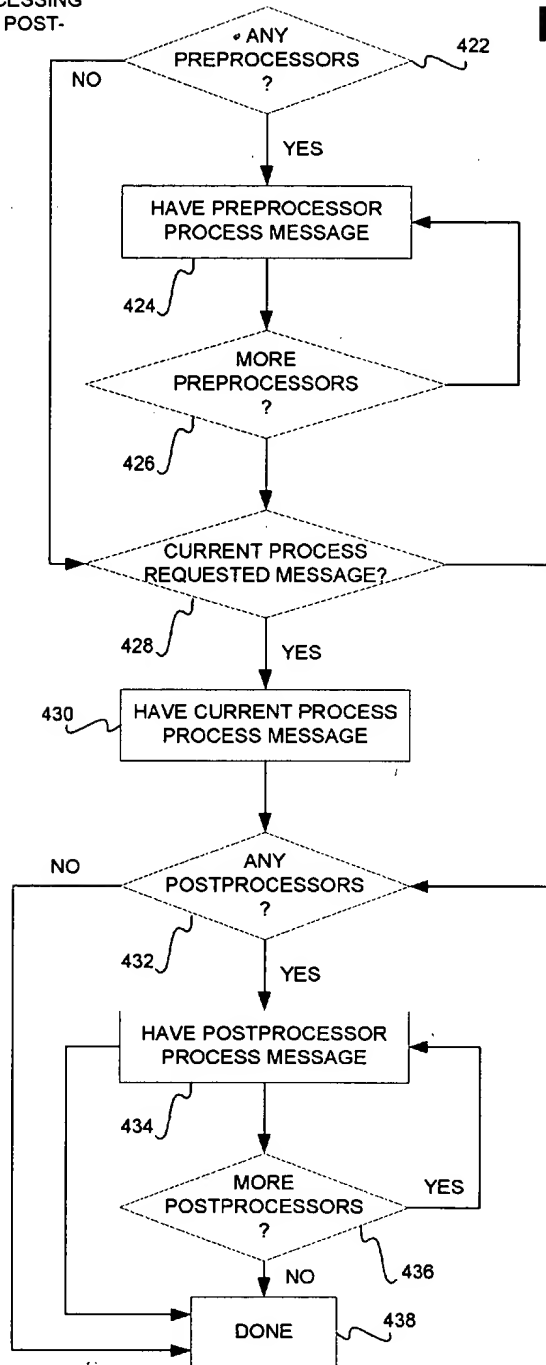
09647-06401 282T8960

FIG 4(a)



MESSAGE PROCESSING
WITH PRE- AND POST-
PROCESSORS

FIG 4(b)



09681782-060401

FIG 5

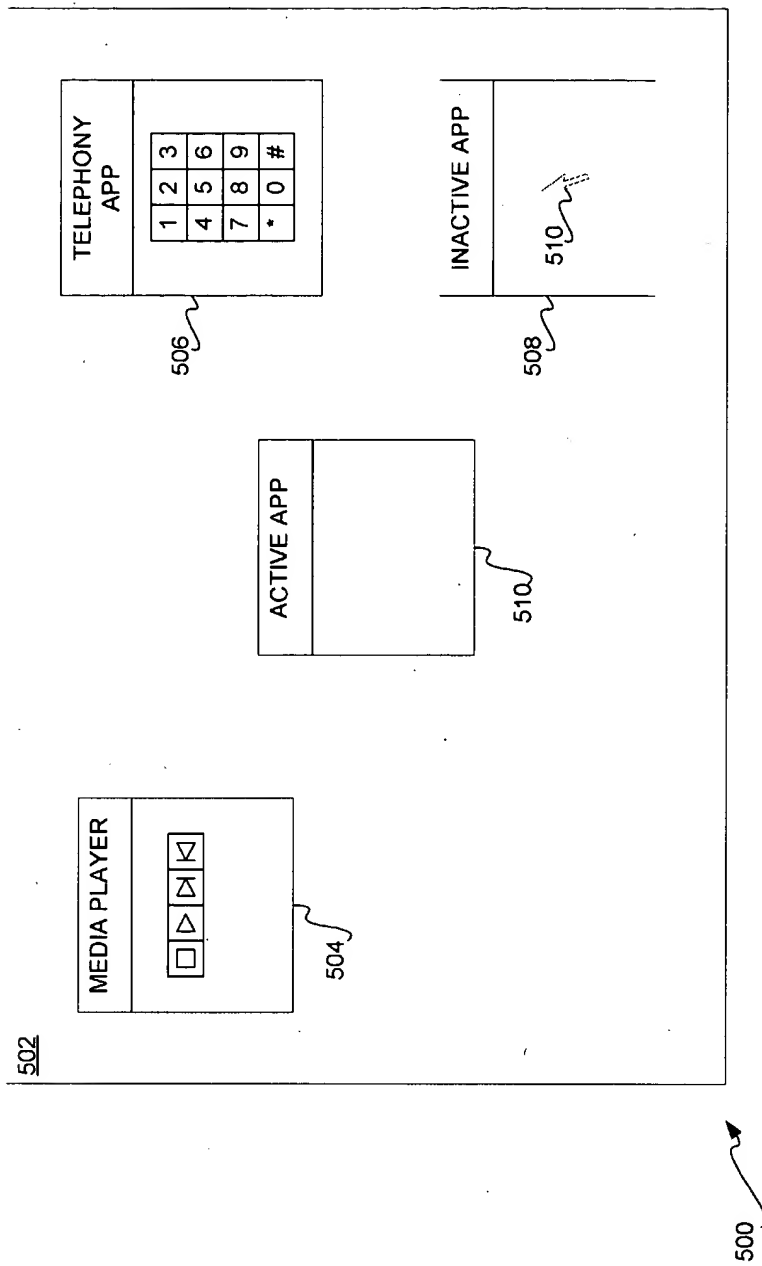


FIG 6

